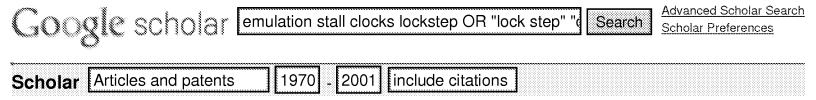
Sign in



Did you mean: emulation stall clocks lockstep OR "lockstep" "device under test"

Your search - emulation stall clocks lockstep OR "lock step" "device under test" - did not match any articles published between 1970 and 2001.

#### Suggestions:

Try searching over a larger date range.

Make sure all words are spelled correctly.

Try different keywords.

Try more general keywords.

Try fewer keywords.

Try your query on the entire web

Did you mean to search for: emulation stall clocks lockstep OR "lockstep" "device under test"

emulation stall clocks lockstep OR " Search

Go to Google Home - About Google - About Google Scholar

©2010 Google

Sign in

| Scholar | Articles and patents | 1970 - 2001 | Include citations | Search | Search | Scholar Preferences | Scholar | Search | Search | Scholar Preferences | Scholar | Search | Search | Scholar | Search | Search | Scholar | Search |

Your search - emulation sleep stall clocks "device under test" - did not match any articles published between 1970 and 2001.

#### Suggestions:

Try searching over a larger date range.

Make sure all words are spelled correctly.

Try different keywords.

Try more general keywords.

Try fewer keywords.

Try your query on the entire web

emulation sleep stall clocks "device Search

Go to Google Home - About Google - About Google Scholar

©2010 Google

# Caoogle scholar "cypress semiconductor" DUT sleep OR stall "c

Search

Advanced Scholar Search Scholar Preferences

Scholar | Articles and patents

anytime include citations Results 1 - 7 of 7. (0.08 sec)

## System and a method for checking lock step consistency between an in circuit ...

C Nemecek - US Patent 6,922,821, 2005 - Google Patents

... IS IN PROGRESS (75) Inventor: Craig Nemecek, Seattle, WA (US) (73) Assignee: Cypress **Semiconductor** Corp., San ... to provide interfacing to the base station and the device under test (DUT). ... is not emulating), or when system encounters a breakpoint (eg, sleep, stall, 5 internal ...

Cited by 1 - Related articles - All 2 versions

#### Reducing **sleep** current in a capacitance sensing system

DG Wright - 2008 - freepatentsonline.com

... provide an advantage over conventional capacitance sensing systems by reducing sleep current to ... System on a Chip (PSoC®) processing device, developed by Cypress Semiconductor Corporation, San ... in a single direction onto a device under test ("DUT") capacitor, capacitor ...

### In-circuit emulator with gatekeeper based halt control

C Nemecek, S Roe - US Patent 7,236,921, 2007 - Google Patents

... (73) Assignee: Cypress Semiconductor Corporation, 716 ... If the microcontroller is not in a sleep mode, the gatekeeper simply queues a halt command and ... CYPR-CD00186; "Method for Breaking Execution of Test Code in a DUT and Emulator Chip Essentially Simultaneously and ...

All 2 versions

#### In-circuit emulator with gatekeeper for watchdog timer

C Nemecek, S Roe - US Patent 7,162,410, 2007 - Google Patents

... Insenser Farre et al. .. Mann 716/17 ... 717/124 Steve Roe, Woodinville, WA (US)

6,922,821 R1 \* 7/2005 Nemecek 716/4 (73) Assignee: Cypress Semiconductor

Corporation, 6.957.180 R1 \* 10/2005 Nemecek .... 703/28 San Jose ...

Related articles - All 2 versions

#### Low pin count solution using capacitance sensing matrix for keyboard architecture

L Hua, J Xiao Ping - US Patent App. 11/440,924, 2006 - Google Patents

... 75) Inventors: Liu Hua, Shanghai (CN); Jiang XiaoPing, Shanghai (CN) Correspondence Address:

# BLAKELY SOKOLOFF TAYLOR & ZAFMAN 1279 OAKMEAD PARKWAY SUNNYVALE, CA 94085-4040 (73) Assignee: **Cypress Semiconductor** Corporation (21) Appl. ...

All 2 versions

#### Apparatus and method for detecting multiple buttons with one pin

L GuangHai - US Patent App. 11/477,179, 2006 - Google Patents

... WITH ONE PIN Publication Classification (75) Inventor: Li GuangHai, Shanghai (CN) Correspondence Address: BLAKELY SOKOLOFF TAYLOR & ZAFMAN 1279 OAKMEAD

PARKWAY SUNNYVALE, CA 94085-4040 (73) Assignee: Cypress Semiconductor Corporation ( ...

All 2 versions

#### Preventing unintentional activation of a sensor element of a sensing device

J Konstas - 2008 - freepatentsonline.com

... The oscillator/clocks block 206 provides **clock signals** to one or more of the ... Programmable System on a Chip (PSoC™) processing device, manufactured by **Cypress Semiconductor** Corporation, San ... 357 in a single direction onto a device under test ("**DUT**") capacitor, capacitor ...

"cypress semiconductor" DUT sleep Search

Go to Google Home - About Google - About Google Scholar

©2010 Google